

**ABSTRACT****Inventor: Andrzej WOZNIAK****Applicant: BULL S.A.**

The invention concerns relates to a method for the automatic recognition of simulation configurations of integrated circuits under design comprising at least two components connected to one another directly or indirectly, for the functional verification of the circuits through simulation tests, characterized in that The method comprises:

- a step for the acquisition of a simulation configuration by a first server manager (14) called a “server manager” (14),
- a step for the sending of a request by a second client manager, called a “client manager” (11), to the server manager (14),
- a step for the sending of a response by the server manager (14) to the client manager (11), and
- a step for the comparison by the client manager (11) of the response with the requirements of the test, followed by a step for the disabling, activation and/or modification of certain parts of the test by the client manager (11) in order to adapt the test to the configuration or signaling an error if the test cannot be adapted to the configuration.

**Fig. 2**